

XA-9721A

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Keisuke TSUKAMOTO et al.

Appln. No.:

Filed: HEREWITH

For: A SEMICONDUCTOR INTEGRATED CIRCUIT DEVICE AND A  
METHOD OF MANUFACTURING THE SAME

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PRELIMINARY AMENDMENT

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Sir:

Prior to examination, please amend the above-  
identified patent application as indicated below.

Amendments to the specification begin on page 2 of  
this paper.

Amendments to the claims are reflected in the listing  
of claims which begins on page 3 of this paper.

Remarks begin on page 7 of this paper.